

Recent IBM Patents

The following patents were recently issued by the countries in which the inventions were made.

France

7,305,428
7,347,206

G. Bonneau
H. Nussbaumer

Process and Apparatus for Multiplexing
Digital Filters

Germany

2,553,517
2,603,688
2,657,561

W. Haug, R. Schnadt and R. Clemen
A. Frosch, W. Mannsdoerfer and
C. Scheuing
K. Heuber, F. Wernicke, W. Klein,
S. I. Wiedmann and K. Najmann

Delay Circuit with Field Effect Transistors
Arrangement for Absorbing Vibrations
Restore Reference Circuit Arrangement for a Semiconductor

United Kingdom

1,503,971
1,503,972
1,504,112

1,504,113
1,504,980
1,505,603

R. D. Commander and J. R. Taylor
W. J. Case and R. D. Commander
T. E. Robinson, N. Kingdom-
Hocking, M. C. Pinnell, R. Sheeler,
J. Simmon, B. H. Middleton and
P. H. Benson
P. H. Benson and P. H. Snagge
C. Williams and P. J. Evans
C. Paradine and G. W. Robinson

Data Storage Apparatus
Data Storage Apparatus
Interactive Enquiry Systems

Transport Reservation and Ticketing System
Raster Display Apparatus
Improvements in Data Processing Systems

United States

4,087,832
4,095,881
4,096,290
4,096,355

4,096,378
4,096,400
4,096,534

4,096,535
4,096,566

4,096,573

4,096,578

4,096,626
4,097,115

4,097,753
4,097,765

4,097,802

4,097,856
4,097,872
4,097,873
4,098,032
4,098,605

C. G. Jambotkar
R. A. Maddox
E. C. Fredericks
E. H. Rothausser and D. Wild

A. W. Vinal
M. F. M. S. Ferry and D. F. Reynes
D. E. Brownback, C. S. Nomura,
D. P. Sheehan and M. M. Siverling
J. L. Highnote
J.-C. Borie, A. Couder, A. Dauby,
M. Demange, G. Lebizay and
M. Lechaczynski
A. R. Heller and R. L. Sites

C. D. Malkemes

C. E. Olsen and L. J. Serpa
R. L. Garwin and J. J. Levine

P. W. Cook, J. T. Parrish and
S. E. Schuster
H. H. Zappe

N. J. Mazzeo and D. A. Thompson

W. R. Lamoureux and J. B. Trushell
F. P. Giordano and F. Hendriks
V. C. Martin
M. Olbert
D. A. Nepela, D. W. Rice and
J. C. Suits

Two-Phase Charge Coupled Device Structure
Efficient Illumination System
Resist Mask Formation Process with Haloalkyl Methacrylate Copolymers
Common Channel Access Method for a Plurality of Data Stations in a Data
Transmission System and Circuit for Implementing the Method
Distorted Two Frequency Coded Data Interpreting Method and Apparatus
Inductive Load Driving Amplifier
Track Accessing Circuitry for a Disk File with Switchable Filter

Rotary Transformer with Unique Physical and Electrical Characteristics
Modular Signal Processor Having a Hierarchical Structure

DLAT Synonym Control Means for Common Portions of All Address
Spaces
Data System with Microprocessor Featuring Multiplexed Data Transfer
and Repeat Cycle Driving Arrangement
Method of Making Multi-Layer Photosensitive Glass Ceramic Charge Plate
Optical Scanning Device for Producing a Multiple Line Scan Using a
Linear Array of Sources and a Textured Scanned Surface
Comparator Circuit for a C-2C A/D and D/A Converter

Electronically Alterable Non-Latching Josephson And, Or, Nand, Nor
Logic Circuit
Magnetoresistive Field Sensor with a Magnetic Shield which Prevents
Sensor Response at Fields Below Saturation of the Shield
Gas Panel Single Ended Drive Systems
Axial Droplet Aspirator
Ink Jet Printer for Selectively Printing Different Resolutions
Method of Forming Air Bearing Rails of Head Assemblies
Ferromagnetic Palladium Alloys

4,098,618	B. L. Crowder, W. R. Hunter and D. W. Ormond, Jr.	Method of Manufacturing Semiconductor Devices in Which Oxide Regions are Formed by an Oxidation Mask Disposed Directly On a Substrate Damaged by Ion Implantation
4,099,062	J. R. Kitcher	Electron Beam Lithography Process
4,099,160	H. L. Flagg	Error Location Apparatus and Methods
4,099,175	B. J. Rubin	Charge-Coupled Device Digital-to-Analog Converter
4,099,254	D. R. Andrews, A. F. Neel II and D. W. Zegafuse	Method and Apparatus for Electronic Collation
4,099,257	R. F. Arnold, Y. Dishon, N. K. Ouchi and M. I. Schor	Markov Processor for Context Encoding from Given Characters and for Character Decoding From Given Contexts
4,099,866	R. O. Jones and M. H. Ulrich	Focal Adjustment on a Single-Focus Lens in a Continuously Variable Magnification System
4,099,987	C. G. Jambotkar	Fabricating Integrated Circuits Incorporating High-Performance Bipolar Transistors
4,100,039	J. J. Caricchio, Jr. and E. R. York	Method for Plating Palladium-Nickel Alloy
4,100,403	J. S. Eggenberger and A. M. Patel	Method and Means for Discriminating Between Systematic and Noise- Induced Error in Data Extracted from Word Organized Memory Arrays
4,100,447	F. Tsui and K. Tsui	Addressing of Gas Discharge Display Devices
4,100,486	B. N. Casowitz, M. D. Cowan, C. B. Humphreys and A. V. S. Satya	Monitor for Semiconductor Diffusion Operations
4,100,605	B. J. Holman	Error Status Reporting